

**OakGate Technology announces the availability of its cutting-edge
Memory Fencing Technology for its industry leading storage test platform**

The world leader for SSD test and validation platforms delivers a patent-pending memory fencing technology to detect errors that occur when an SSD accesses memory locations outside the area specified by the device driver

Loomis, California, April 2, 2019 – OakGate Technology, the industry leader for advanced, high-performance test solutions for flash storage, announces the release of its patent-pending memory fencing technology.

By incorporating memory fencing technology to its already feature-rich storage test platform, OakGate is once again pushing the boundaries to provide a critical error detection tool for its customers. Prior to this breakthrough, users could not set memory address ranges (memory fences or barriers) for the purpose of error detection or security in either test or production environments.

“The OakGate team has a rich heritage of pioneering key storage I/O technologies that later became the de-facto industry standards. We are continuing this tradition with our innovative memory fencing technology,” stated Bob Weisickle, co-founder and CTO of OakGate Technology. “With this patent-pending technology, we are now able to offer customers a valuable tool to detect errors caused by illegal memory access,” added Mr. Weisickle.

The memory fencing capability is enabled by our patent-pending method of mapping physical memory locations through the I/O memory management unit (IOMMU) to a set of virtual addresses for specific devices on the PCIe backplane. Currently in a test system used to test direct memory access (DMA) devices, the available address range is limited to a numerical range associated with the size of the physical memory. This limitation does not allow the test system to validate memory access beyond that limited range. OakGate’s DMA mapping technology enables the test system to fence, detect, and authenticate memory access throughout the full DMA address range.

OakGate’s memory fencing technology has been incorporated in the company’s fourth generation SVF software suite, Enduro/SVF Pro v4.2. By selecting this option within Enduro (the user interface), the user can check all memory accessed by the device under test (DUT) to the range which has been specified for that device. If the DUT attempts a memory access outside the specified range, the OakGate software flags the operation as an error. The OakGate software detects and categorizes these errors, and provides the address and operations of the DUT that is violating the memory access.

Key benefits of the memory fencing technology are two-fold:

1. In a test or development environment, it detects the errors that occurred when a DUT accesses memory space outside of the area specified for the device. These errors range

from data corruption to total system failure. Normally, these errors can be difficult and time consuming to debug, but with this technology, these errors can be caught early in the development process, averting more complex issues that are nearly impossible to root cause.

2. In the field, it detects when a device purposely accessed memory outside the specified range – preventing system hacking, unauthorized data access, and/or infecting a system with a virus.

The memory fencing technology is currently available with Enduro/SVF Pro v4.2 and later. Please contact OakGate Technology for any additional information.

About OakGate Technology

OakGate Technology Inc. (www.oakgatetech.com) is the #1 test solution for solid state storage. The privately held company is the recognized leader in test and validation tools for solid state storage. OakGate's highly-capable and flexible platforms support all popular storage protocols and are based on advanced, proprietary software, and industry standard hardware. OakGate's products have been deployed by top tier components suppliers, drive manufacturers, and storage system OEMs worldwide. Based in Loomis, California, OakGate Technology was founded in 2008 by industry veterans from storage and server industries.

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